

Invitation for the post of Research Associates in the area of VLSI-ASIC-FPGA-DFT Design.

Working with IIITB and Chipcron Pvt. Ltd. (Startup incubated at IIITB). Interested candidates can apply before 30th October, 2023.

Designation	Research Associate
Job Description	<ul style="list-style-type: none">• Design and Evaluation of the Digital Combinatorial Designs and Memory Designs through ASIC flow.• Linux, Tcl, Python scripting.• Working on FPGA and FPGA tools and opensource frameworks.• Working from RTL to GDSII generation using open source ASIC flow and Cadence tools.• Developing framework for Built-in-self-test for FPGA and ASIC.• Developing framework for Built-in-self-repair logic for FPGA and ASIC.• Expected to read and understand research papers and give presentation and develop ideas.
Qualifications, Experience and Skills needed	<ul style="list-style-type: none">• Graduated Masters or working PhD candidate specializing in VLSI.• BTech/BEs from Reputed Institutes (IITs, NITs, IIITs, BITs) with complete VLSI and FPGA flow knowledge, and Programming knowledge will also be considered.• Experience with Linux and programming.• Experience with Open source frameworks understanding.• Experience with CAD tools and ASIC, FPGA tools.• Experience with Verilog, computer architecture and digital design flow.• Ability to communicate in English.
Pay and Allowances	As per norms of the institute
Last date for applications	Interested candidates are requested to fill in the form below along with resume latest by October 30, 2023 https://forms.gle/wXcyWHSso7tq9oH58