IIITB Invites Applications Invited for the Post of Research Assistant in the area of VLSI-ASIC Design. Interested applicants can apply before October 05, 2023.

Designation	Research Assistant
Job Description	- Design and Evaluation of the Digital Combinatorial Designs and Memory Designs through ASIC flow - Running Placement and Routing on the designs using Cadence Tool and other open source tools efficiently - Running AI-ML algorithms in Python and integrating the same for ASIC flow - TCL/TK scripting, - Simulation of the circuits in Verilog.
Qualifications, Experience and Skills needed	- Graduated BE/BTech in ECE or Instrumentation or Electrical from reputed university/institute. - Prior experience in VLSI Cadence Tools or Open-source ASIC flow synthesis tools is preferred. - Experience and Skills - Familiar with TCL/TK Scripting, Verilog and Python Coding, - Basic knowledge and strong Interest in VLSI ASIC Design flow, Verilog programming, and Understanding of Routing and Placement. - Ability to communicate in English
Pay and Allowances	As per norms of the institute
Last date for applications	Interested candidates are requested to fill in the form below along with resume latest by October 05, 2023 https://forms.gle/aJH45A4uxQzYE9Xi7 Kindly contract HR – hr@iiitb.ac.in in case you face an issue in filling the form